

**METHOD OF MAKING A finFET HAVING SUPPRESSED
PARASITIC DEVICE CHARACTERISTICS**
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FIG.1A
(PRIOR ART)

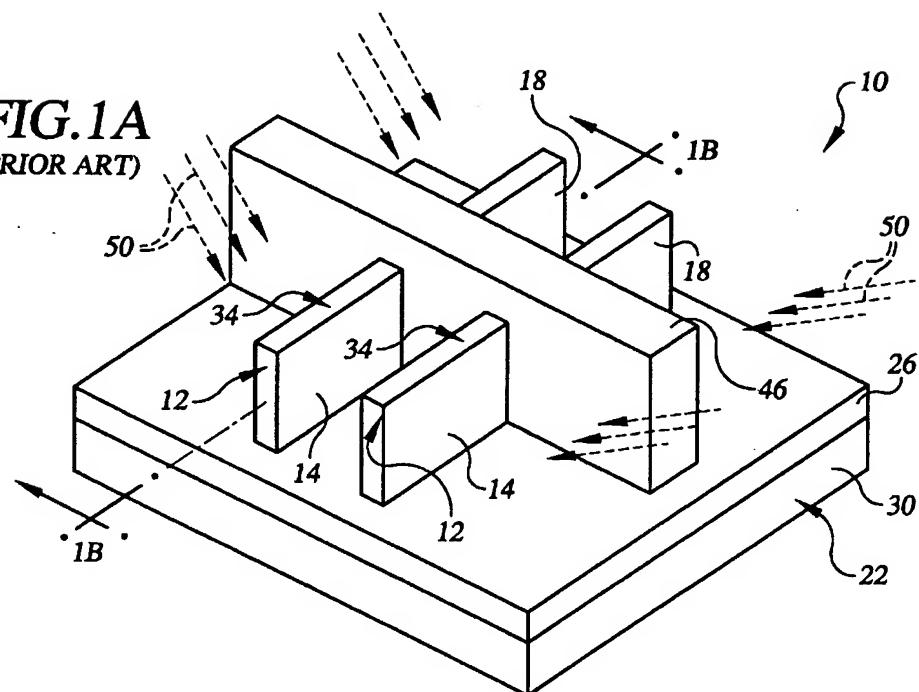
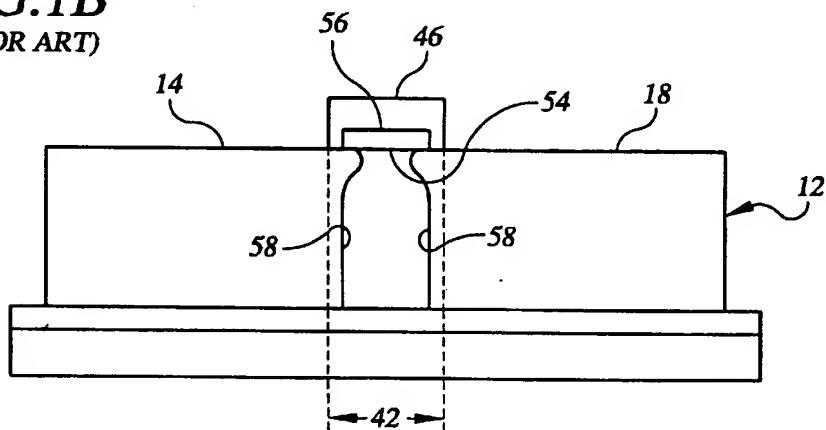


FIG. 1B
(PRIOR ART)



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FIG.2

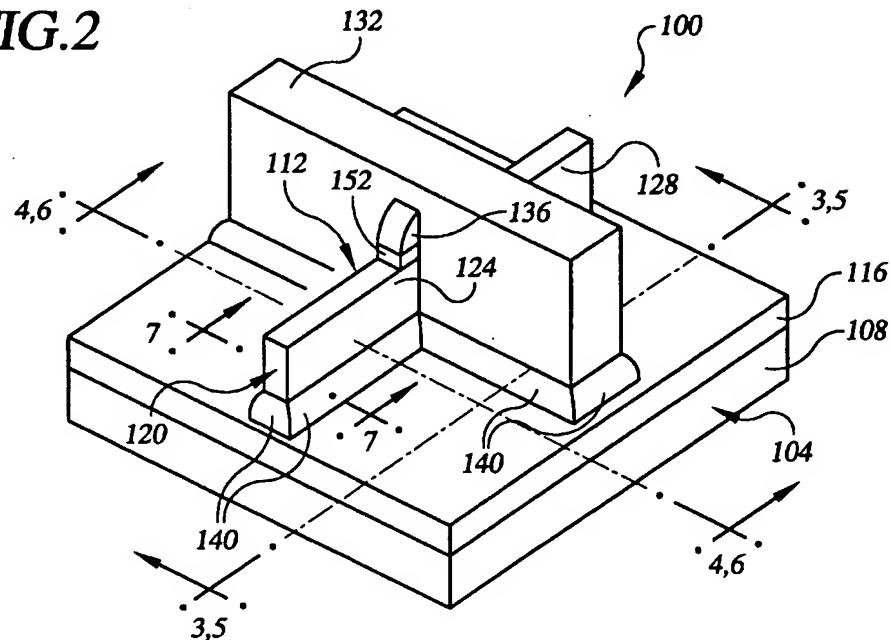
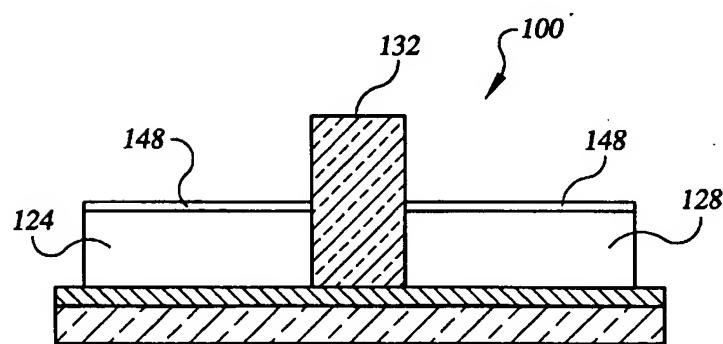


FIG.3



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FIG.4

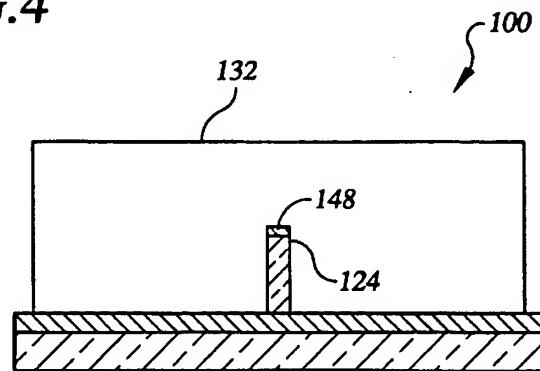
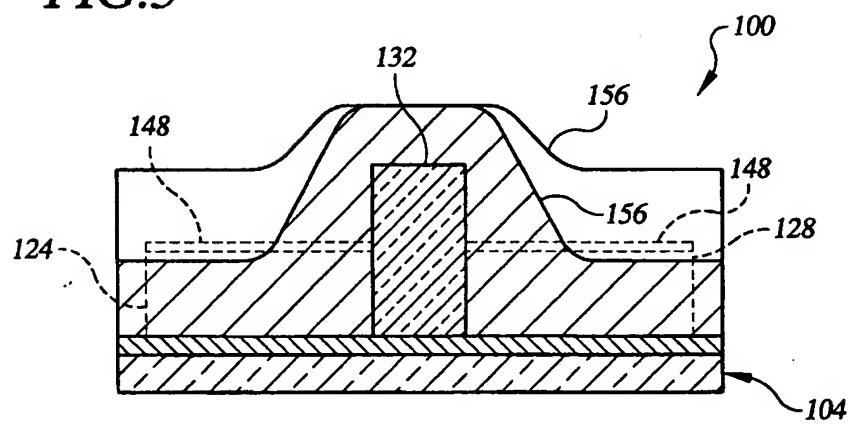


FIG.5



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FIG.6

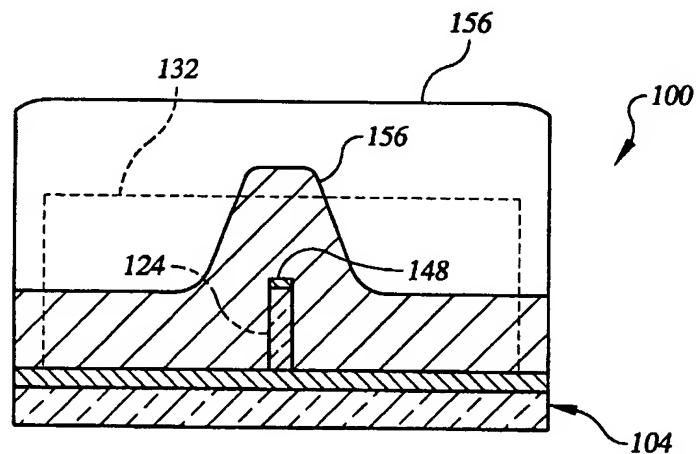
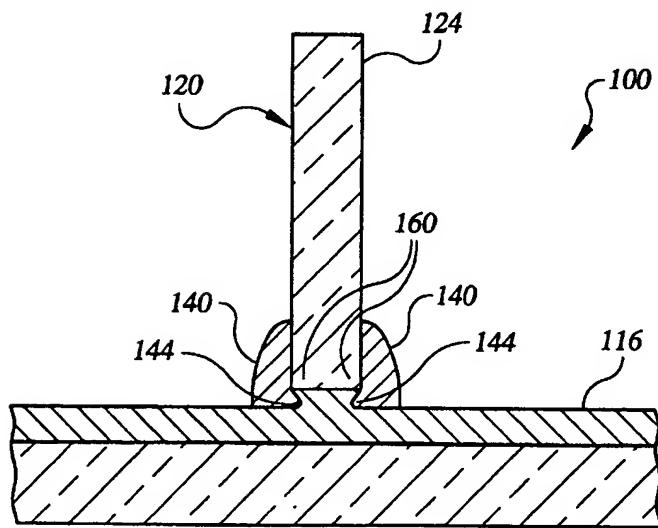


FIG.7



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FIG.8

